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Amendments to the Specification:

Please amend the paragraph which starts on page 1, line 27 and continues to page 2, line 7 to read as follows:

~~WO-01/57554-US 6,653,636~~ discloses a pixel configuration in which the voltage across the pixel photodiode (which is representative of the illumination level) is provided to a source follower circuit arrangement which acts as a unitary gain voltage buffer. The output voltage charges a sampling capacitor, and the gain of the pixel is dependent on the ratio of the capacitance of the sampling capacitor to the pixel capacitance. The circuit operates according to the principle of so-called "Double Correlated Sampling" (DCS). The double sampling approach eliminates noise induced by the resetting of the sampling capacitor and is particularly desirable for low noise amplification. DCS involves sampling the voltage across a sampling capacitor corresponding to a reset condition of the sensor element, so that the subsequent flow of charge to the sampling capacitor is representative of the change in voltage across the sensor element and not dependent on the reset state of the sensor element.